

FIG. 2

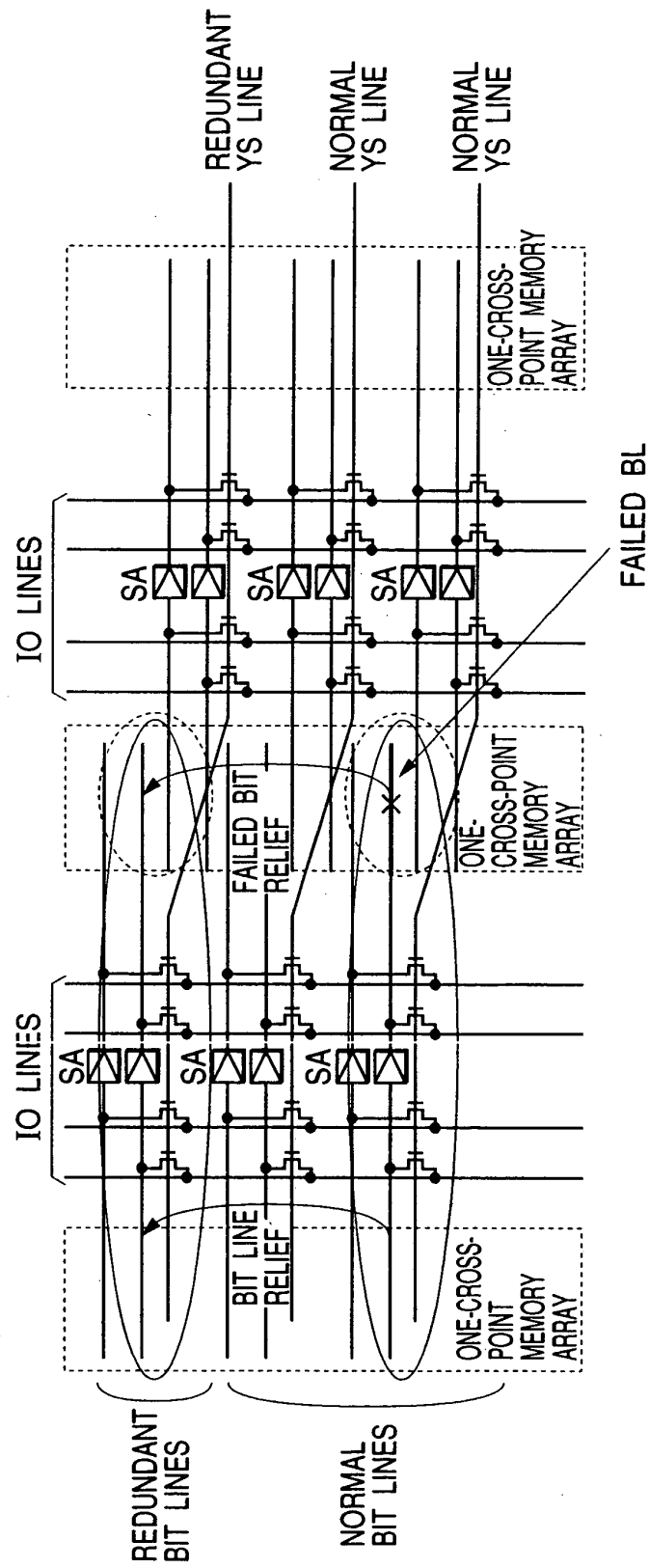


FIG. 3

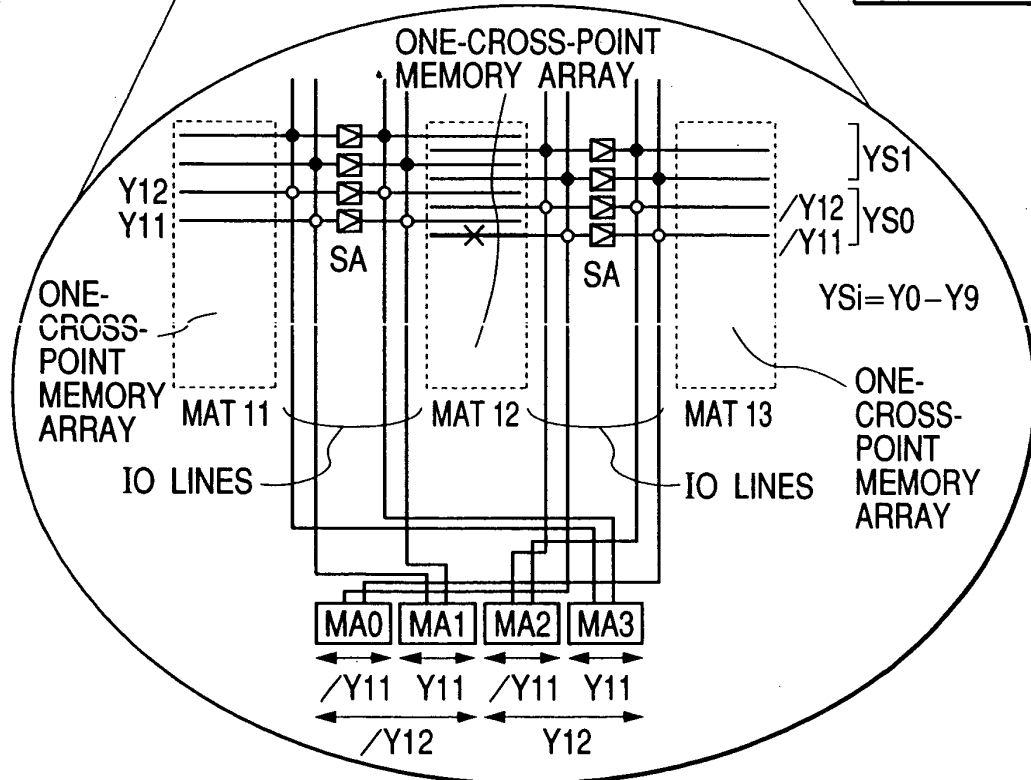
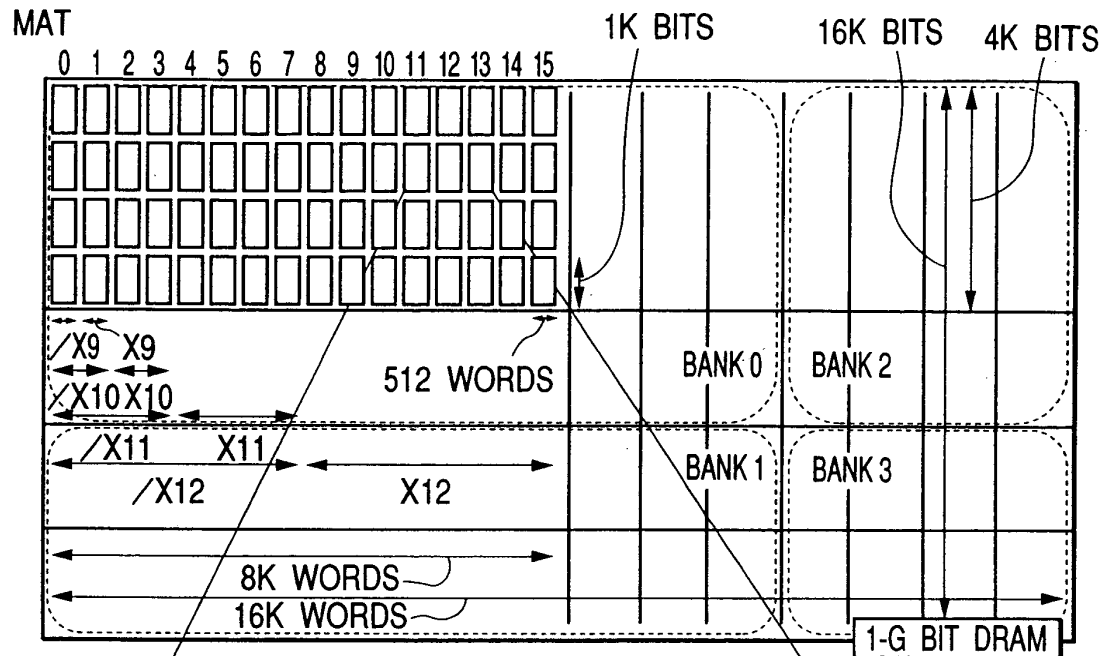


FIG. 4

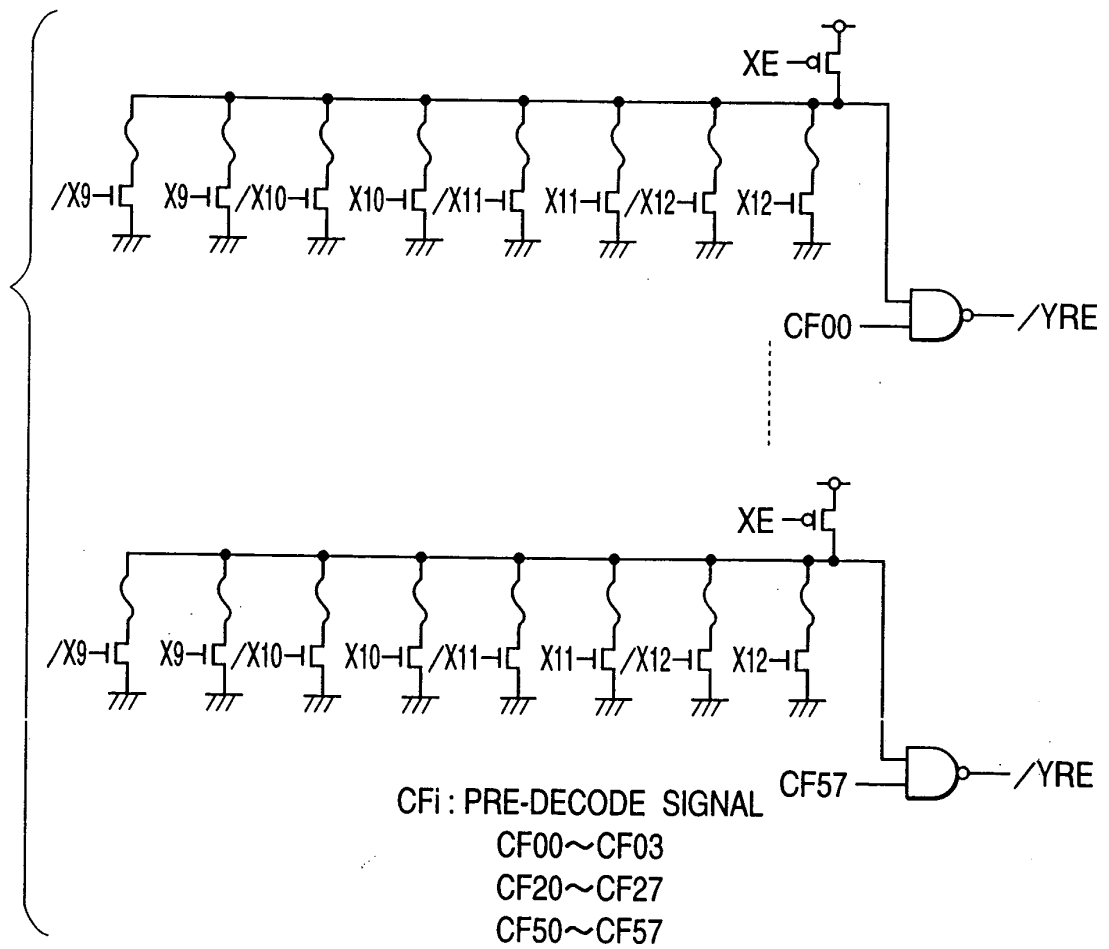
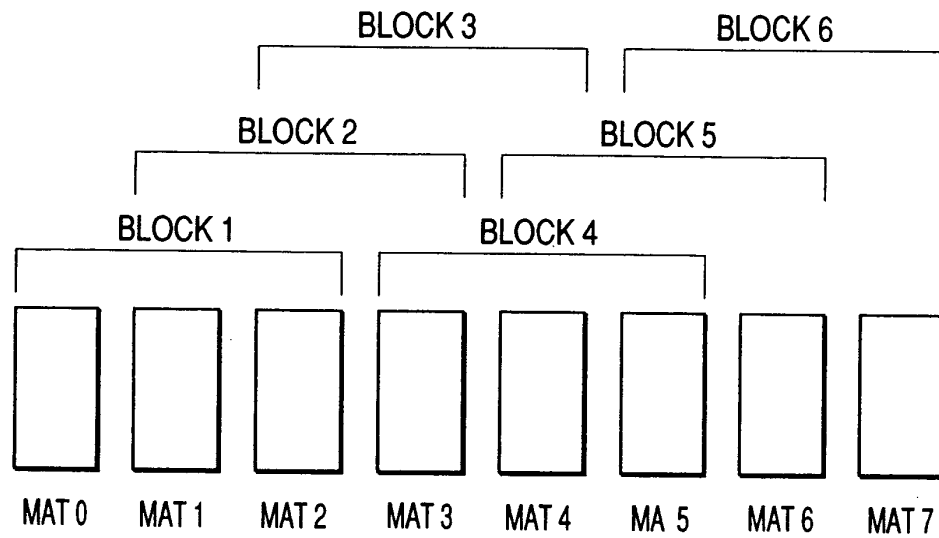


FIG. 5

WHEN FAILED BIT LINE ON MAT 0 OR 1 BLOCK 1 RELIEVED
 WHEN FAILED BIT LINE ON MAT 2 BLOCK 2 RELIEVED
 WHEN FAILED BIT LINE ON MAT 3 BLOCK 3 RELIEVED
 WHEN FAILED BIT LINE ON MAT 4 BLOCK 4 RELIEVED
 WHEN FAILED BIT LINE ON MAT 5 BLOCK 5 RELIEVED
 WHEN FAILED BIT LINE ON MAT 6 OR 7 BLOCK 6 RELIEVED

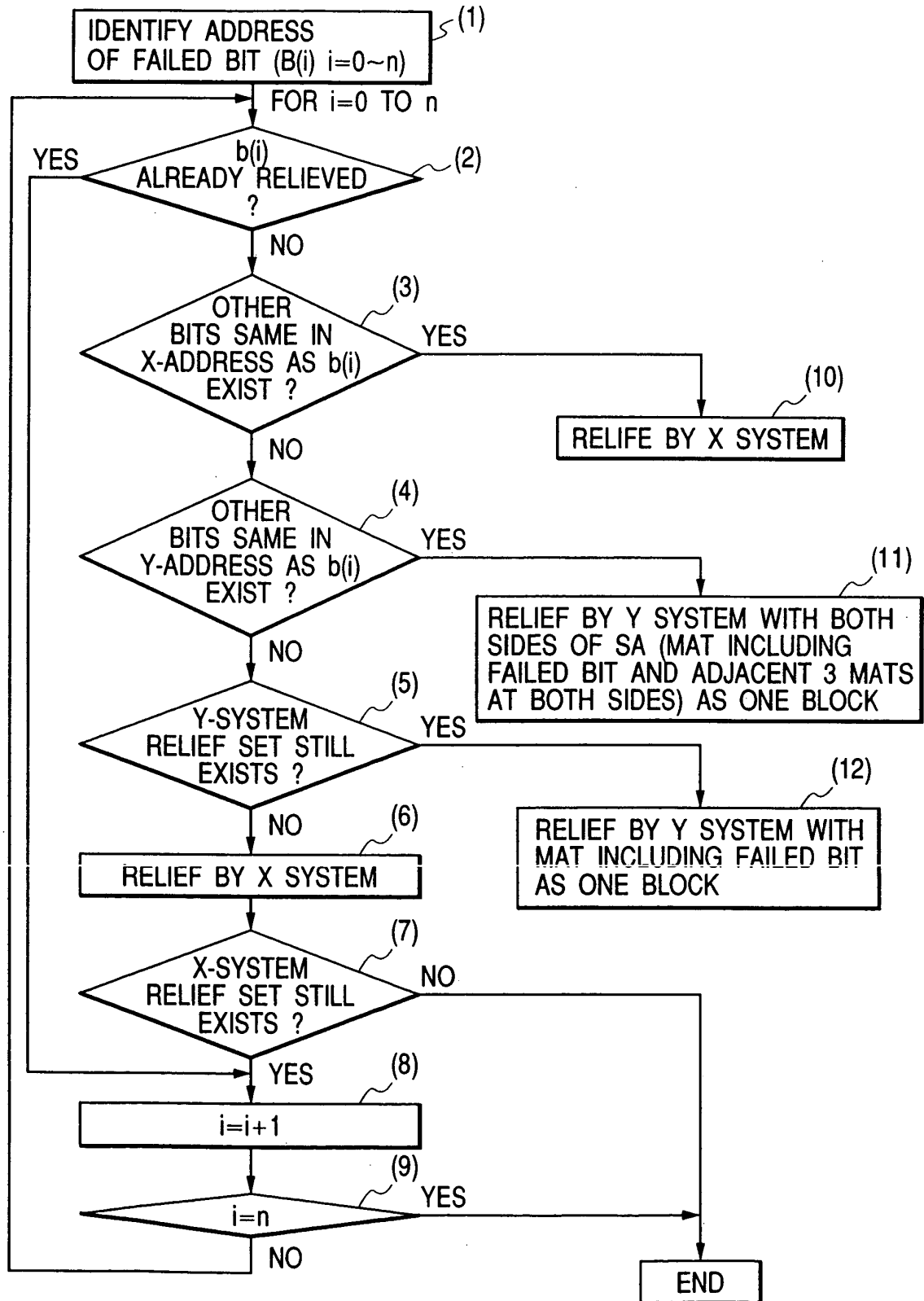
FIG. 6

FIG. 7

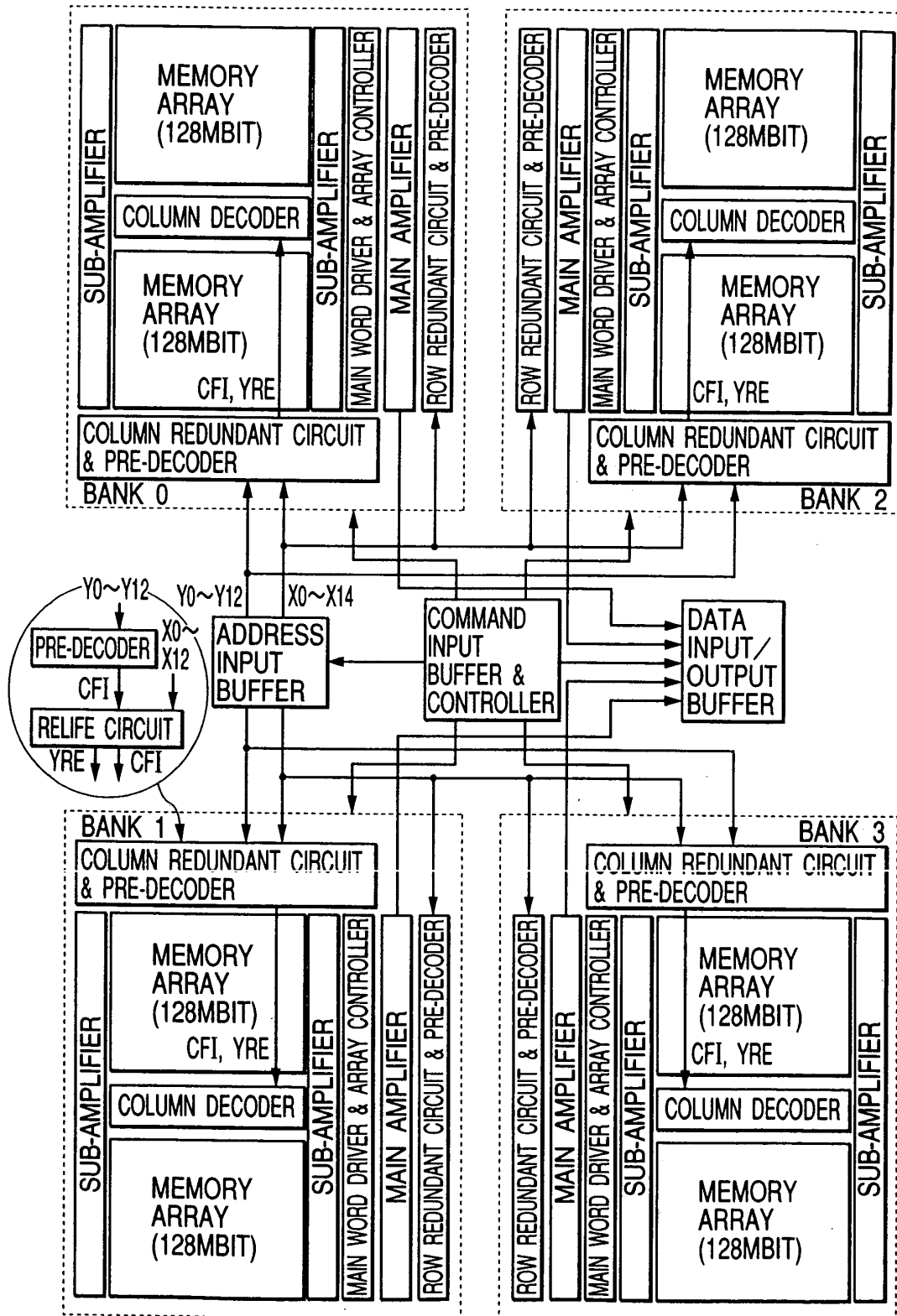


FIG. 8

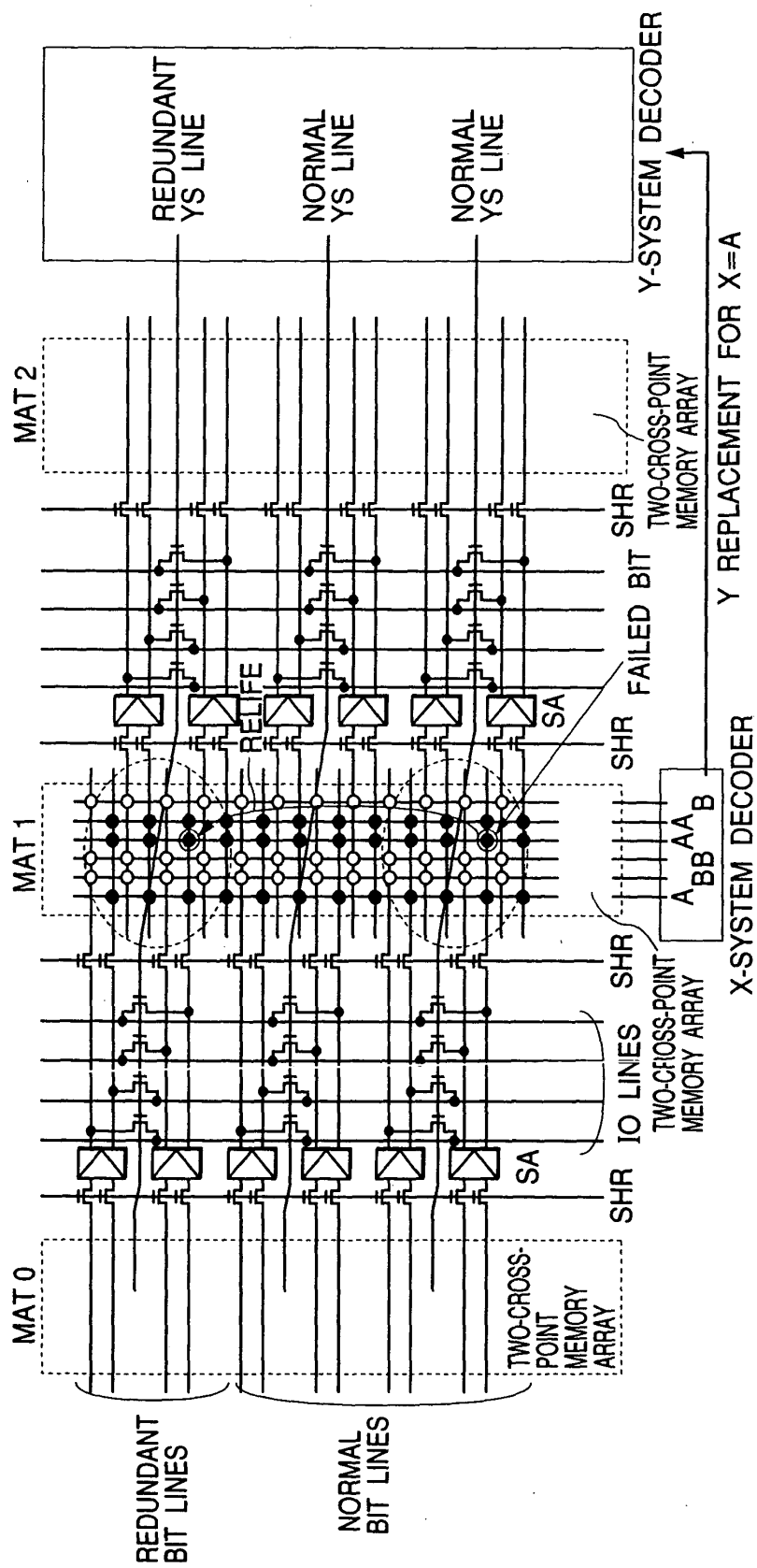


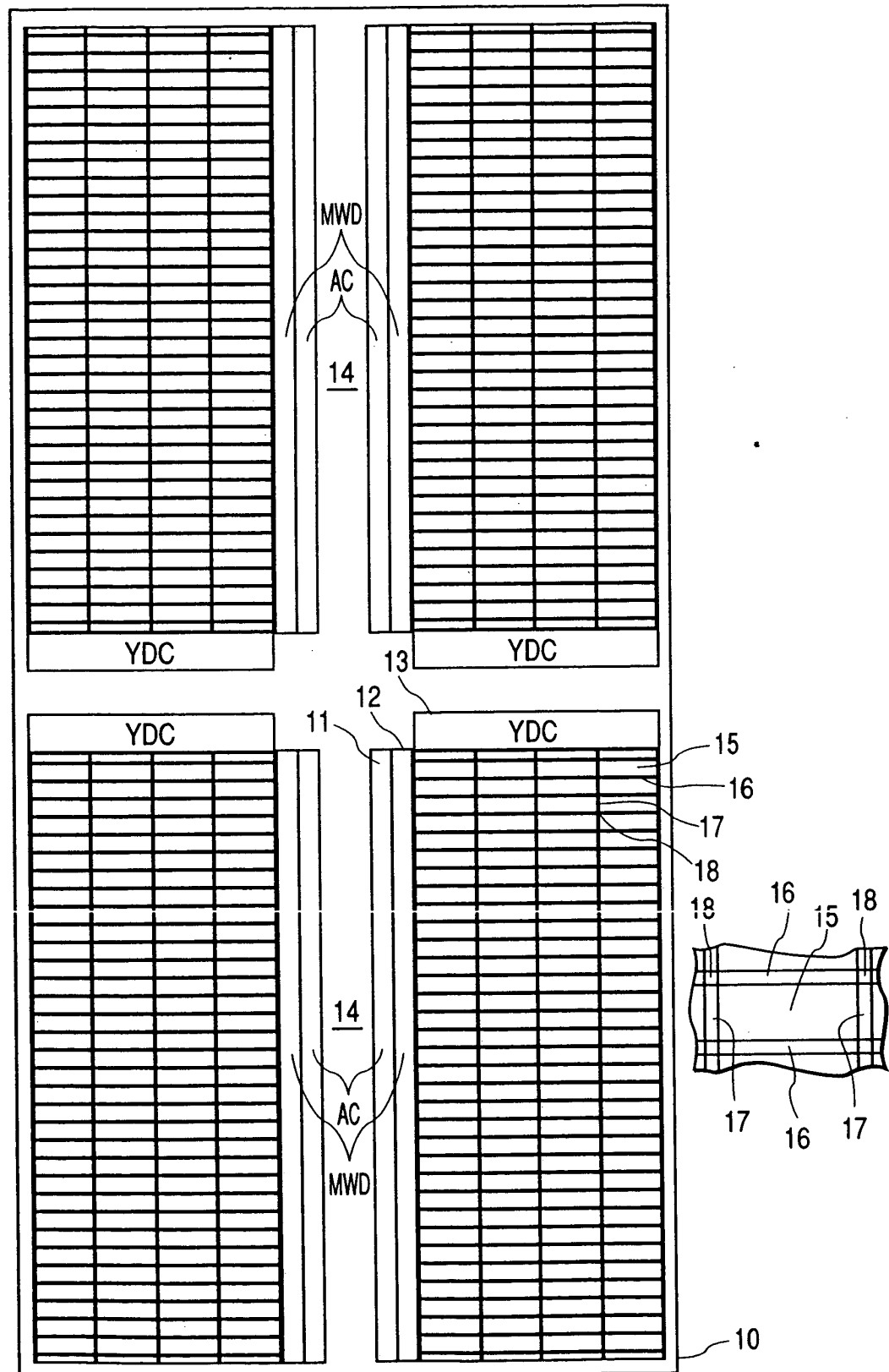
FIG. 9

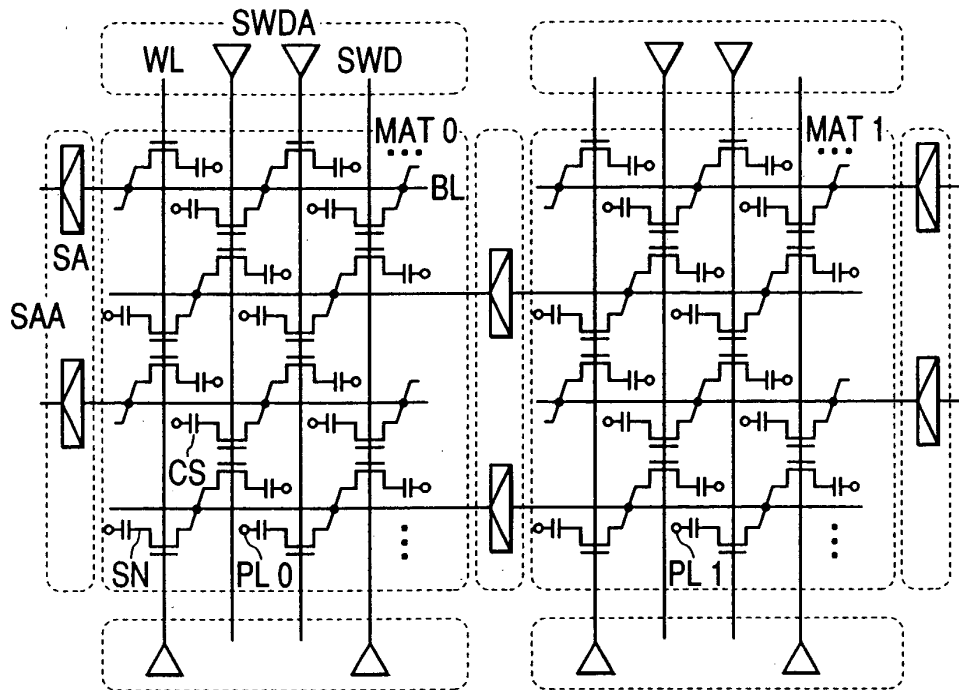
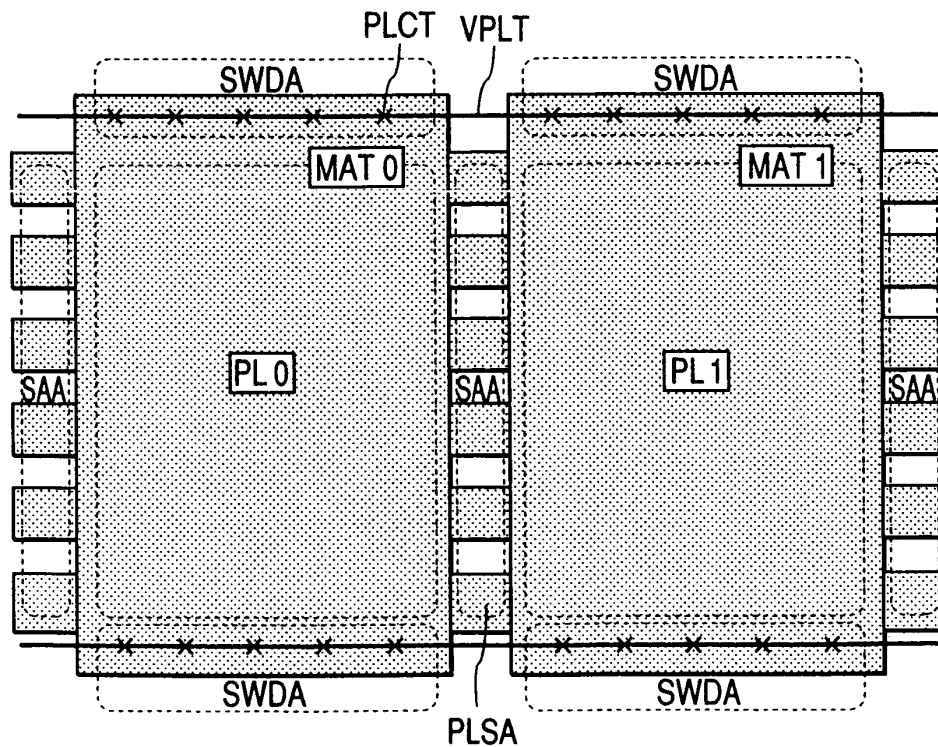
FIG. 10(a)*FIG. 10(b)*

FIG. 11(a)

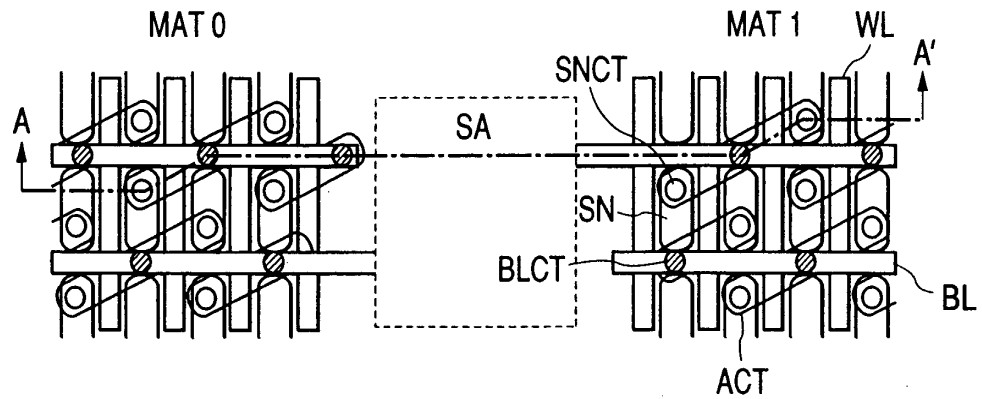


FIG. 11(b)

